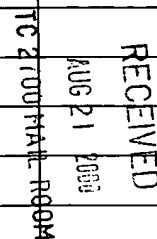
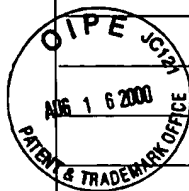


FORM PTO-1449 (Rev. 7-80)		U.S. Department of Commerce Patent and Trademark Office		Atty. Docket No. SCEI 3.0-004		Application No. 09/502,671	
LIST OF PRIOR ART CITED BY APPLICANT <i>(Use several sheets if necessary)</i>				Applicant Suzuoki, et al.		Filing Date: February 11, 2000 Group <del>2787</del> 2183	
U.S. PATENT DOCUMENTS							
*EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
FOREIGN PATENT DOCUMENTS							
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
EC	AL JP9035084	2/7/97	Japan	—	—	X *	
EC	AM JP10003466	1/6/98	Japan	—	—	X **	
EC	AN EPO867809	9/30/98	Europe	—	—		
EC	AO EP0871142	10/14/98	Europe	—	—		
	AP						
OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
EC	AR	* Corresponding U.S. Patent No. 5,943,057 enclosed. ** Abstract from Derwent database enclosed.					
	AS						
	AT						
EXAMINER <i>Eric C. [Signature]</i>				DATE CONSIDERED <i>6/4/03</i>			
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FORM PTO-1449  
(Rev. 7-80)U.S. Department of Commerce  
Patent and Trademark OfficeAtty. Docket No.  
SCEI 3.0-004Applicant No.  
09/502,671

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Suzuoki, et al.

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## U.S. PATENT DOCUMENTS

*EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
AA						
AB						

## FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
EC	AC EP 0 715 279 A2	6/5/1996	Europe	—	—	
	AD EP 0 715 277 A2	6/5/1996	Europe	—	—	
	AE					

## OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EC	AF	KEN KUTARAGI, et al., "A Microprocessor with a 128b CPU, 10 Floating-Point MACs, 4 Floating-Point Dividers, and an MPEG2 Decoder," 1999 IEEE International Solid-State Circuits Conference, February 15-17, 1999, 1999 Digest of Technical Papers, Volume 42.
EC	AG	KEN KUTARAGI, et al., "A Microprocessor with a 128b CPU, 10 Floating-Point MACs, 4 Floating-Point Dividers, and an MPEG2 Decoder," 1999 IEEE International Solid-State Circuits Conference, February 15-17, 1999, Slide Supplement 1999 to the Digest of Technical Papers, Volume 42.
EC	AH	MASAAKI OKA, et al., "Designing and Programming the Emotion Engine," IEEE Micro, November/December 1999, Volume 19, Number 6, pp. 20-28.
EC	AI	KEITH DIFENDORFF, "Sony's Emotionally Charged Chip," Microprocessor Report, April 19, 1999, Volume 13, Number 5, pp. 1, 6-11.
EC	AJ	MASAKAZU SUZUOKI, et al., "A Microprocessor with a 128-Bit CPU, Ten Floating-Point MAC's, Four Floating-Point Dividers, and an MPEG-2 Decoder," IEEE Journal of Solid-State Circuits, November 1999, Volume 34, Number 11, pp. 1608-1618.
EC	AK	"Sony Computer Entertainment Announces the Development of an I/O Processor for the Next Generation Playstation that Provides 100% Backwards Compatibility," Sony Computer Entertainment Inc. press release, March 2, 1999, Tokyo.
EC	AL	"Sony Computer Entertainment Announces the Design of the Next Generation Playstation System," Sony Computer Entertainment Inc. press release, March 2, 1999, Tokyo.
EC	AM	"Sony Computer Entertainment Announces the Development of the World's Fastest Graphics Rendering Processor Using Embedded Dram Technology," Sony Computer Entertainment Inc. press release, March 2, 1999, Tokyo.
EC	AN	"Sony Computer Entertainment Announces the World's Fastest 128 Bit CPU 'Emotion Engine™' for the Next Generation Playstation," Sony Computer Entertainment Inc. press release, March 2, 1999, Tokyo.

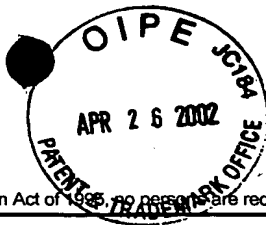
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6 / 9 / 03

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PTO/SB/08A (10-01)

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Substitute for form 1449A/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)				<b>Complete if Known</b>	
				Application Number	09/502,671
				Filing Date	February 11, 2000
				First Named Inventor	Masakazu Suzuoki
				Art Unit	2183
				Examiner Name	Eddie P. Chan
				Attorney Docket Number	SCEI 3.0-004
Sheet	1	of	1		

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
E.C.	BA	CA - 1,304,509		Baldwin		
	BB	CA - 1,198,523		Noches, et al.		
	BC	CA - 2,178,454		Horst		

<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> See attached Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the application number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>

Examiner Signature		Date Considered	6/9/03
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